



PATENT APPLICATION

27m
\$ RCE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichio YUDASAKA et al.

Group Art Unit: 2826

Application No.: 10/717,503

Examiner: T. QUACH

Filed: November 21, 2003

Docket No.: 117849

For: TRANSISTOR, INTEGRATED CIRCUIT,
ELECTRO-OPTIC DEVICE, ELECTRONIC
INSTRUMENT AND METHOD OF
MANUFACTURING A TRANSISTOR

MAIL STOP RCE

**LARGE ENTITY REQUEST FOR
CONTINUED EXAMINATION UNDER 37 C.F.R. §1.114**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. §1.114, Applicants hereby request continued examination.

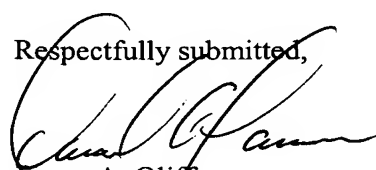
☒ Applicants further request entry and consideration of the attached submissions.

The above-identified application was filed on or after June 8, 1995. Thus, entry is proper under 37 C.F.R. §1.114(d).

Attached hereto is our check no. 171904 in the amount of \$790.00 as payment of the fees set forth in 37 C.F.R. §1.17(e). The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 15-0461. Two duplicate copies of this page are enclosed.

10/21/2005 SZEWDIE1 00000088 10717503

01 FC:1801 790.00 OP
02 FC:1202 250.00 OP

Respectfully submitted,

James A. Oliff
Registration No. 27,075

Daniel A. Tanner, III
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JAO:DAT/scg

Date: October 20, 2005

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<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
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